



MIPICSI Camera RVP Converter Kit

Part Number: UPCR-CAMB-A10-0001

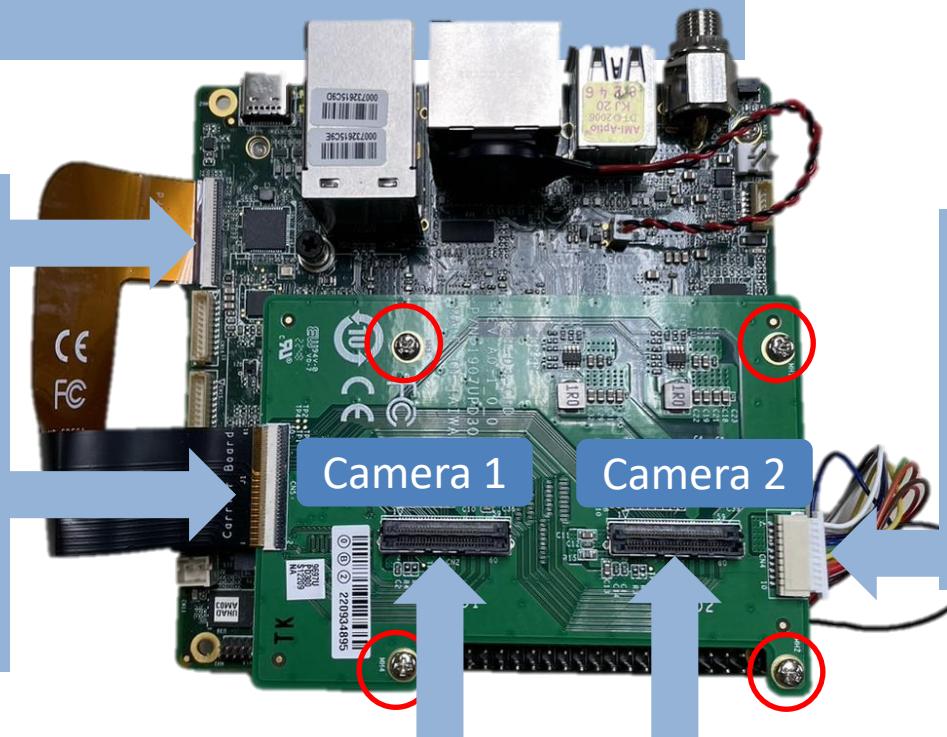
Assembly and BIOS Setup SOP

Part 1: Assembly

Step 1: Fix the RVP Converter board on top of the motherboard with screws and studs.

Step 3: Insert the FPC cable into converter board and motherboard to connect each other via FPC connector at the edge.

Step 2: Insert the power cable into Converter board and motherboard via 10pin wafer at the edge.

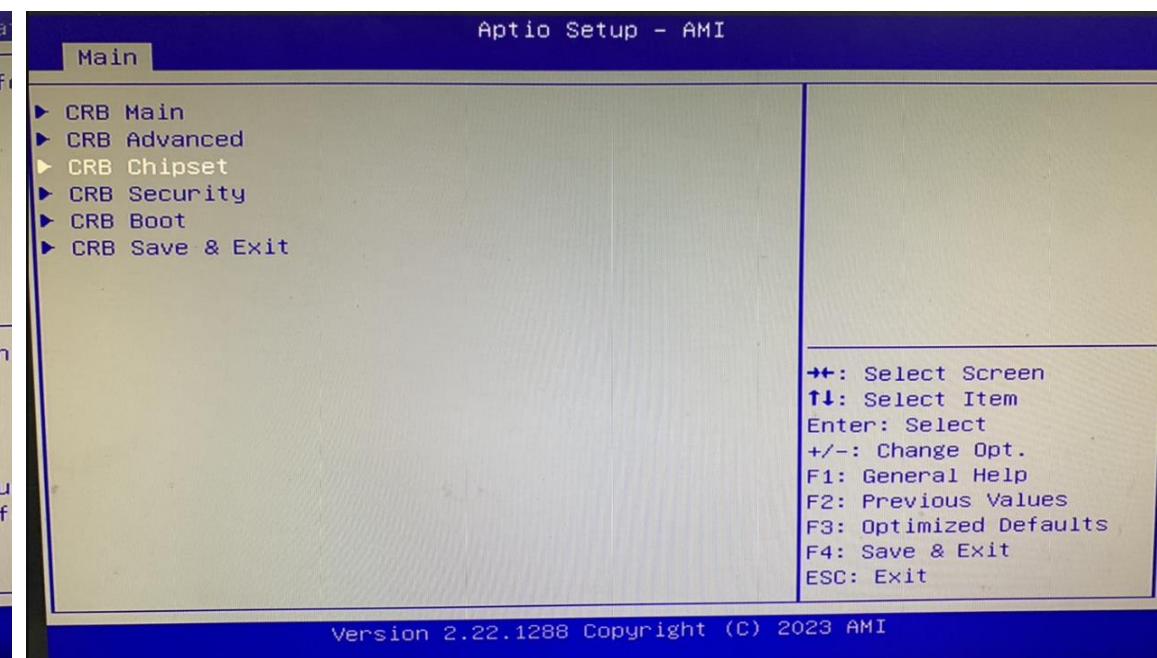
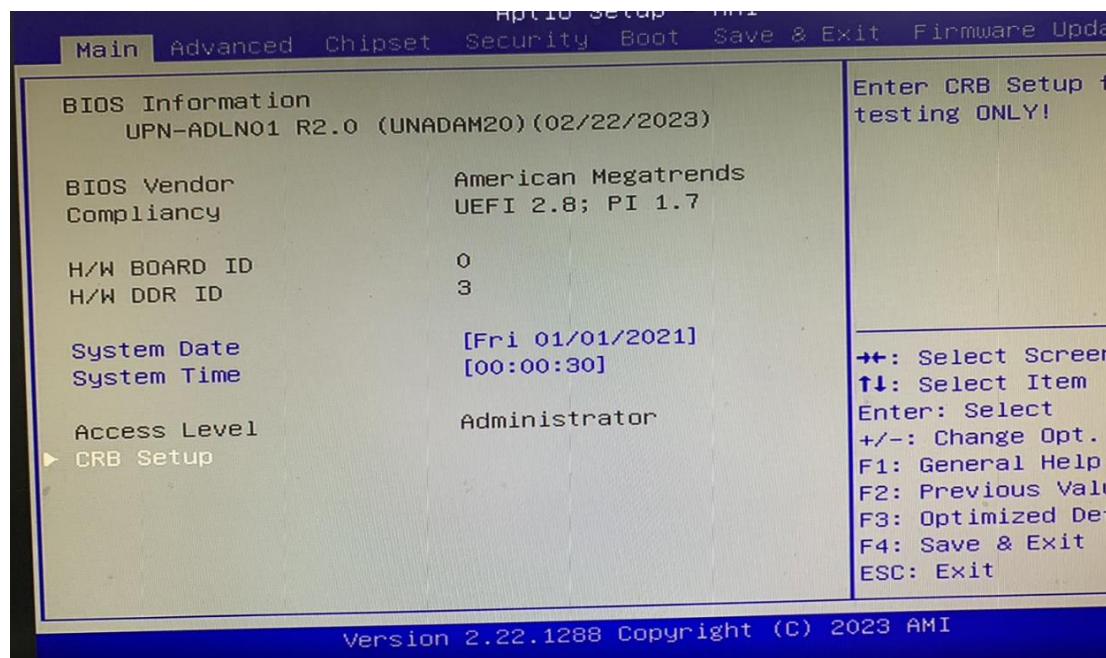


Step 4: Connect your camera module to start!

Part 2: BIOS Setup

D3CM AR0234

1. Key in BIOS password: upassw0rd →CRB Setup→CRB Chipset

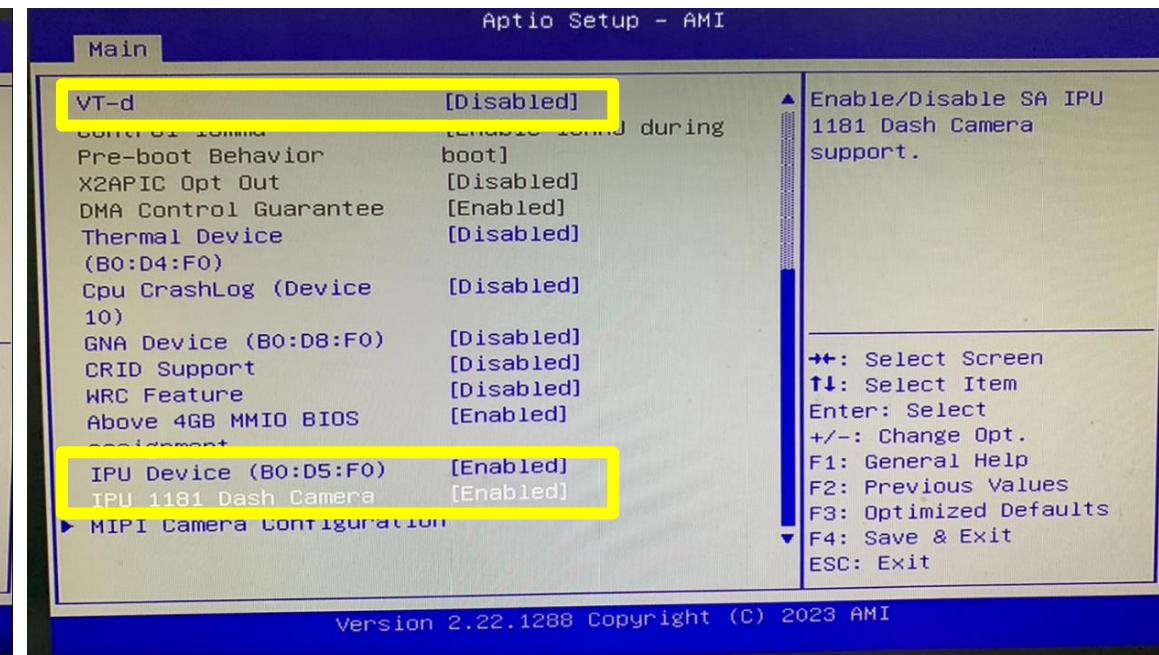
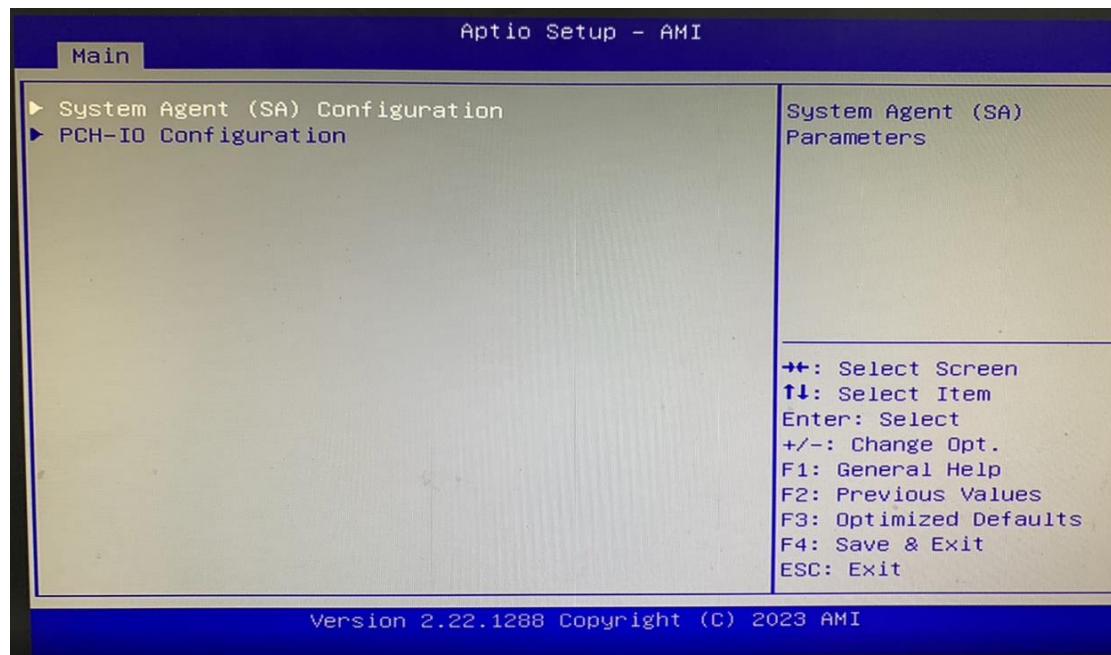


2. Enter System Agent (SA) Configuration

VT-d: **Disabled**

IPU Device(B0:D5:F0): **Enabled**

IPU 1181 Dash Camera: **Enabled**

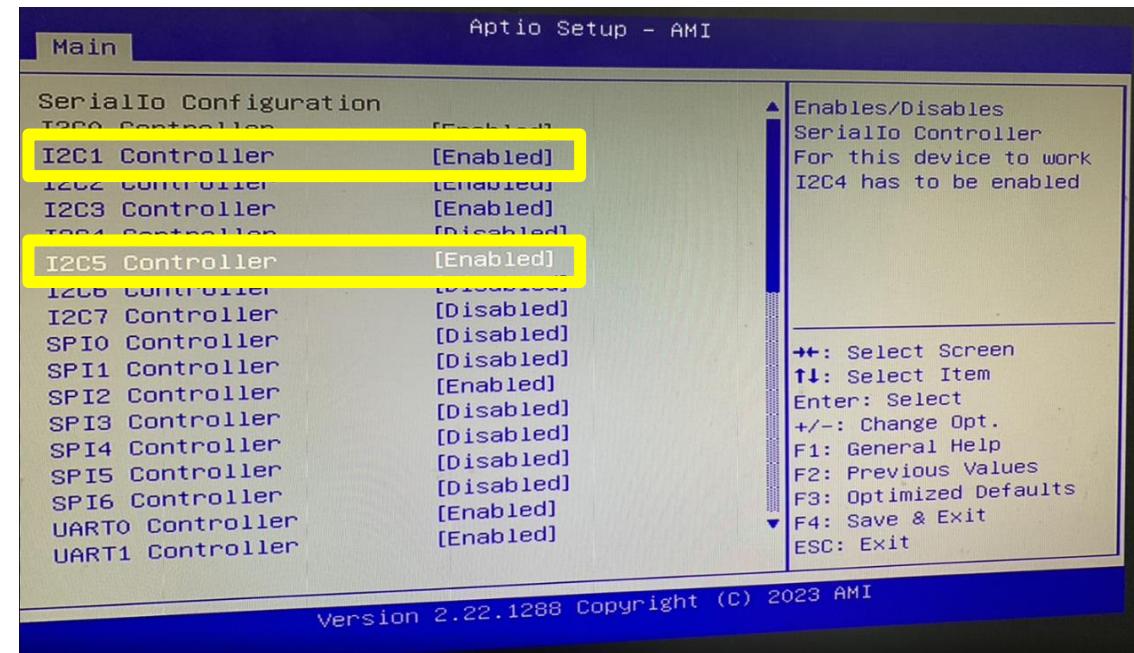
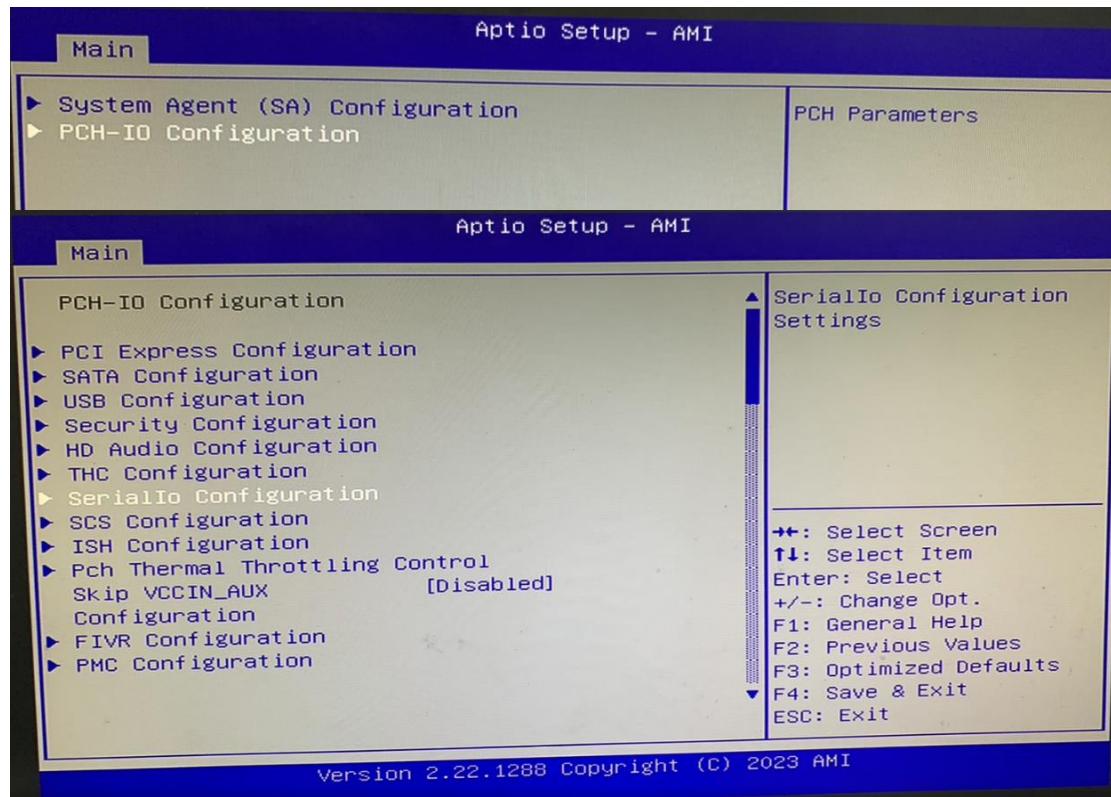


3. Back to main menu, enter PCH-IO

→ SerialIO Configuration

I2C1 Controller: Enabled

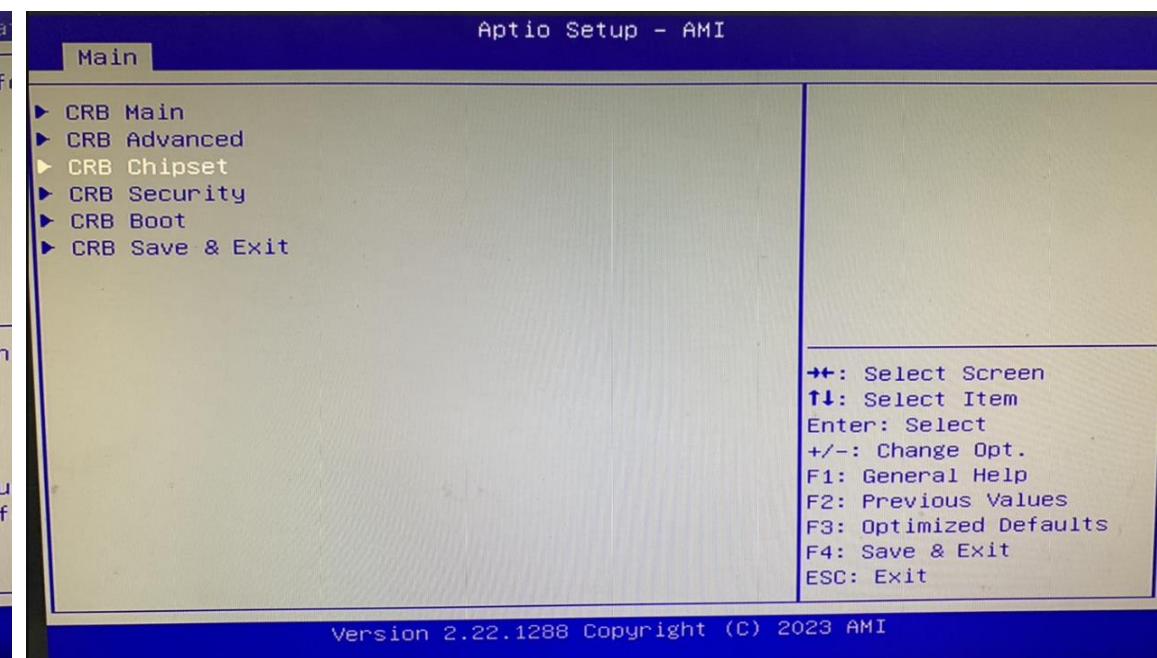
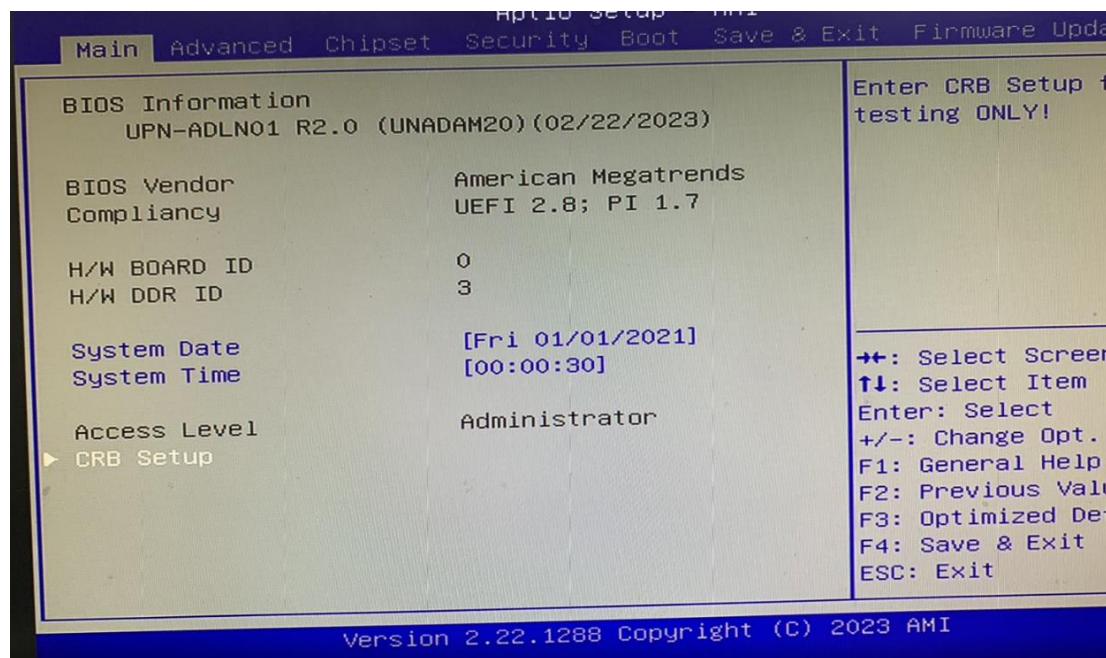
I2C5 Controller: Enabled



After all configuration is completed,
click F4 to save and reset.

LI-IMX415-MIPI-081H

1. Key in BIOS password: upassw0rd →CRB Setup→CRB Chipset

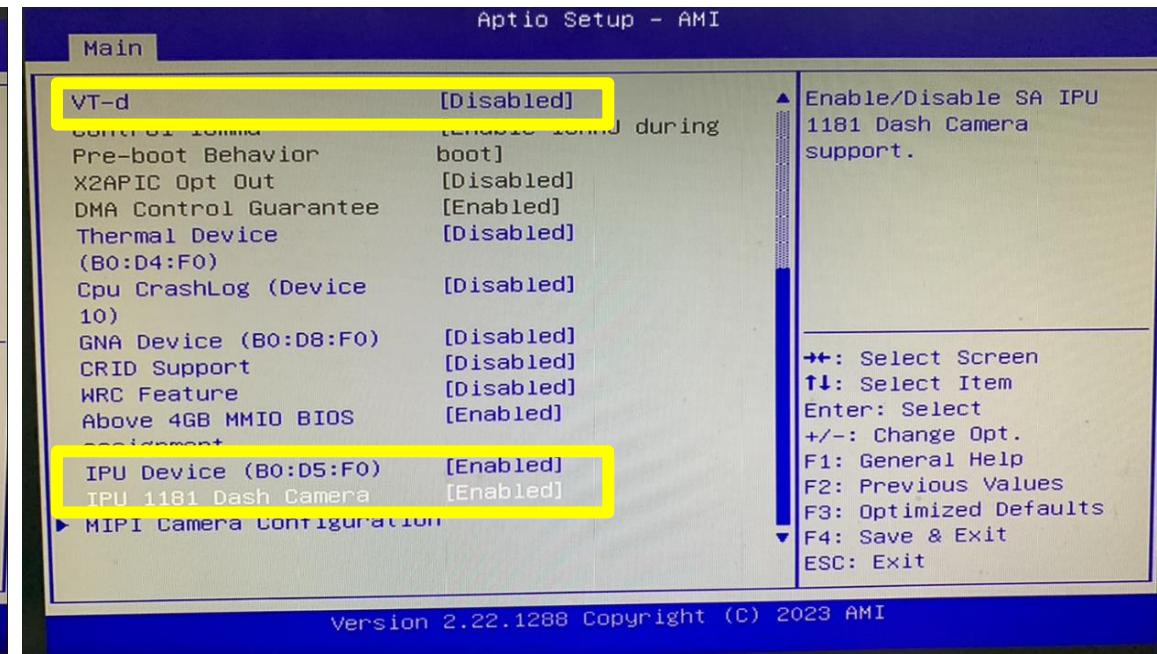
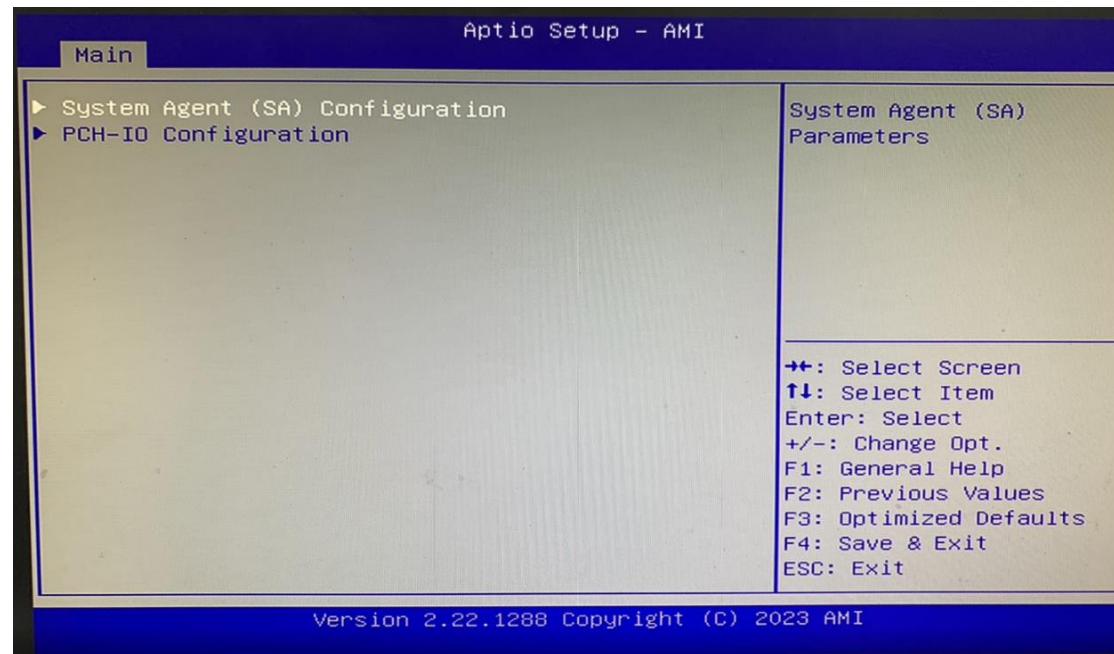


2. Enter System Agent (SA) Configuration

VT-d: Disabled

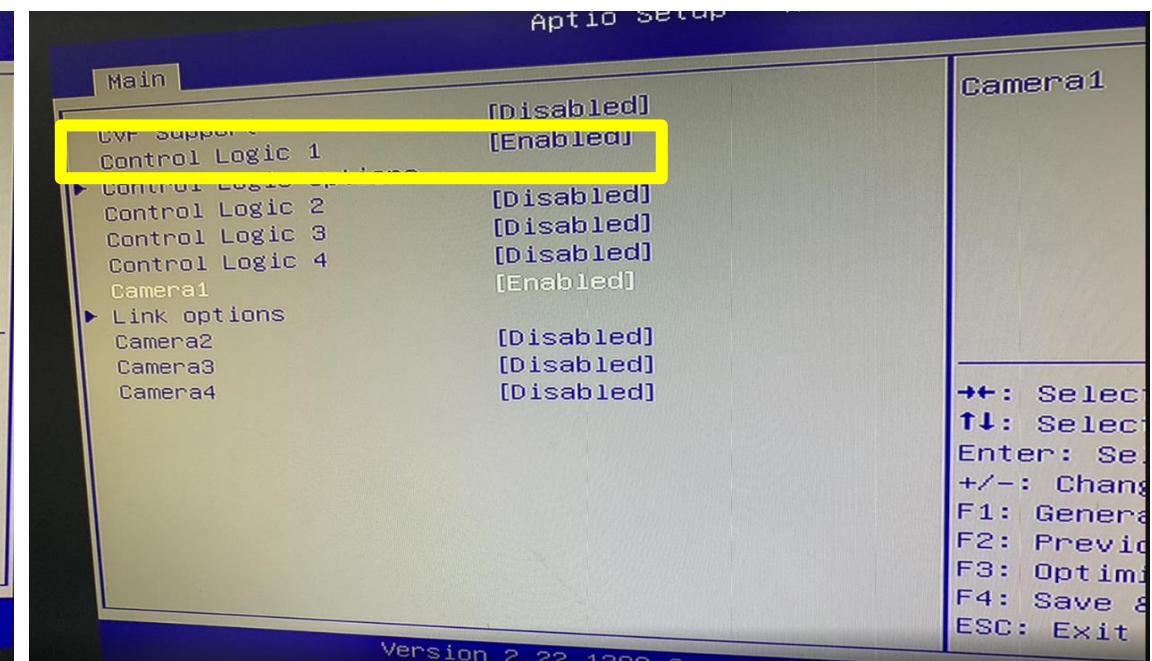
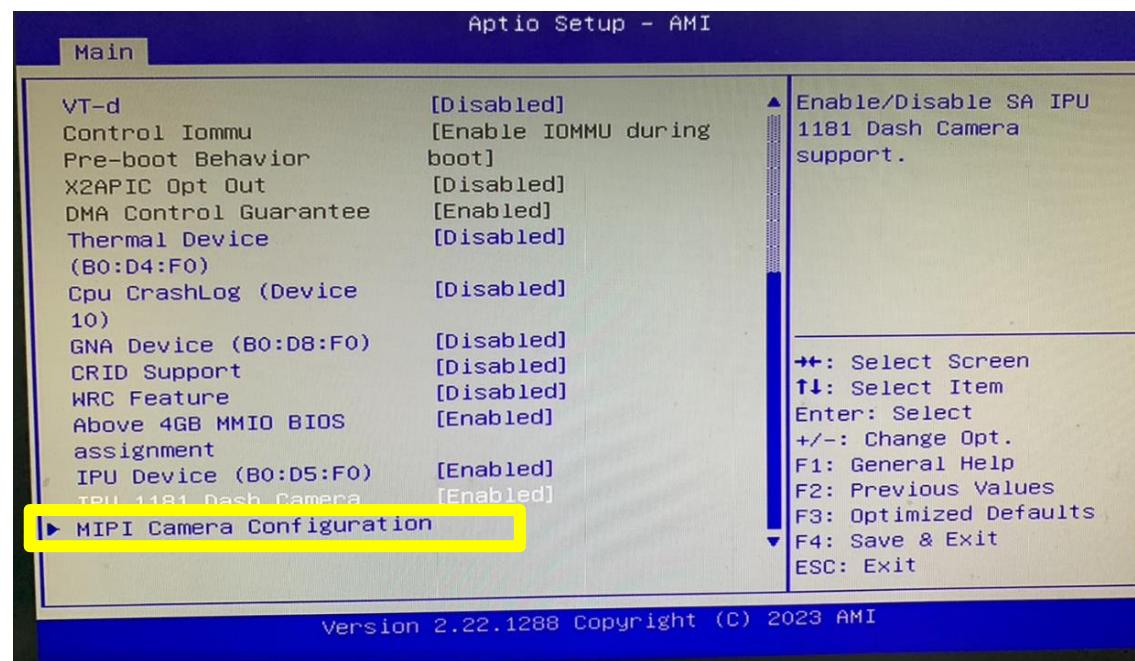
IPU Device(B0:D5:F0): Enabled

IPU 1181 Dash Camera: Enabled



3. Enter MIPI Camera Configuration (for Camera 1)

3-1 Use Camera 1 → Control Logic 1: Enabled



3-2 Control Logic Options

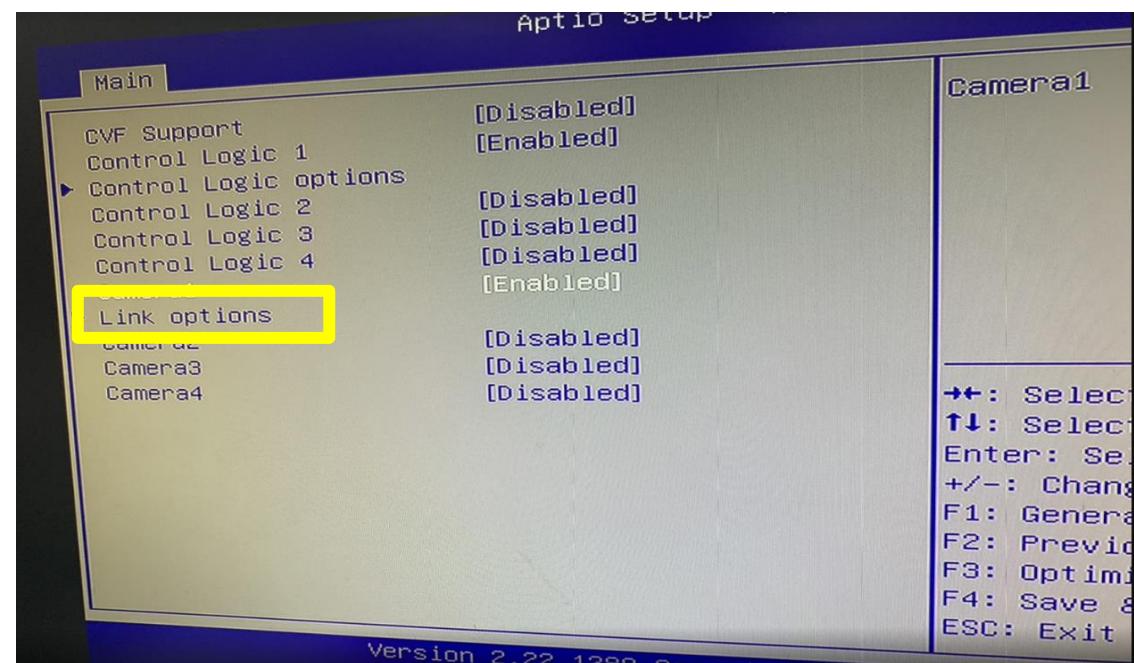
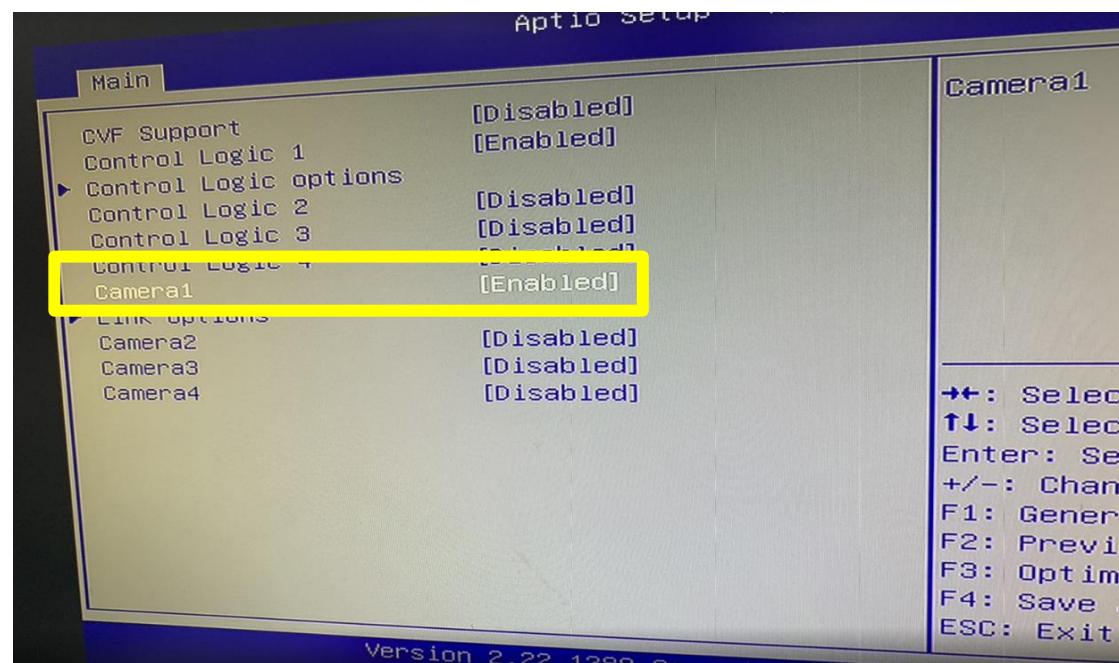
→ Follow the settings in the left picture below:

Aptio Setup	
Main	
CVF Support	[Disabled]
Control Logic 1	[Enabled]
Control Logic options	[Enabled]
Control Logic 2	[Disabled]
Control Logic 3	[Disabled]
Control Logic 4	[Disabled]
Camera1	[Enabled]
Link options	
Camera2	[Disabled]
Camera3	[Disabled]
Camera4	[Disabled]
→++: Select Screen ↑↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Value F3: Optimized Def F4: Save & Exit ESC: Exit	

Main		Initial Value
Control Logic options	[Discrete]	
Control Logic Type	[CRD-D]	
CRD Version	[19.2 MHz]	
Input Clock	[IMGCLKOUT_0]	
PCH Clock Source		
Number of GPIO Pins	2	
GPIO 0		
Group Pad Number	5	
Group Number	[R]	
Function	[Reset]	
Active Value	1	
Initial Value	0	
GPIO 1		
Group Pad Number	23	
Group Number	[B]	
Function	[Power_En]	
Active Value	1	
Initial Value	0	
→++: Select Screen ↑↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Value F3: Optimized Def F4: Save & Exit ESC: Exit		

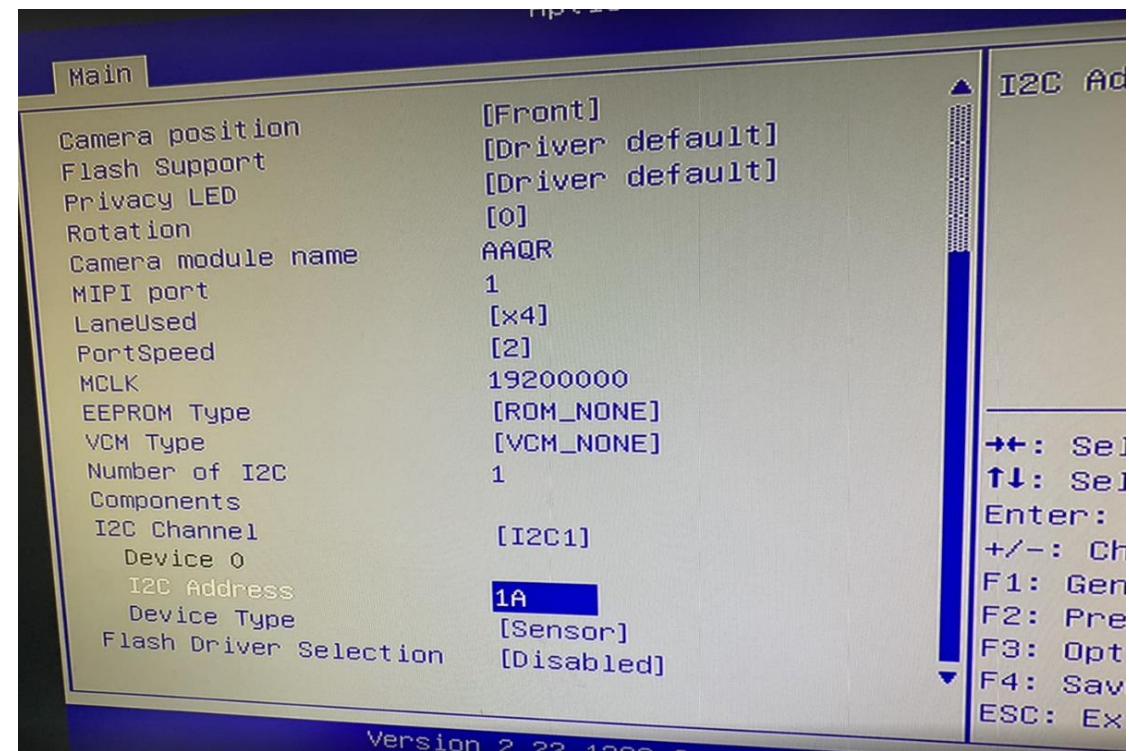
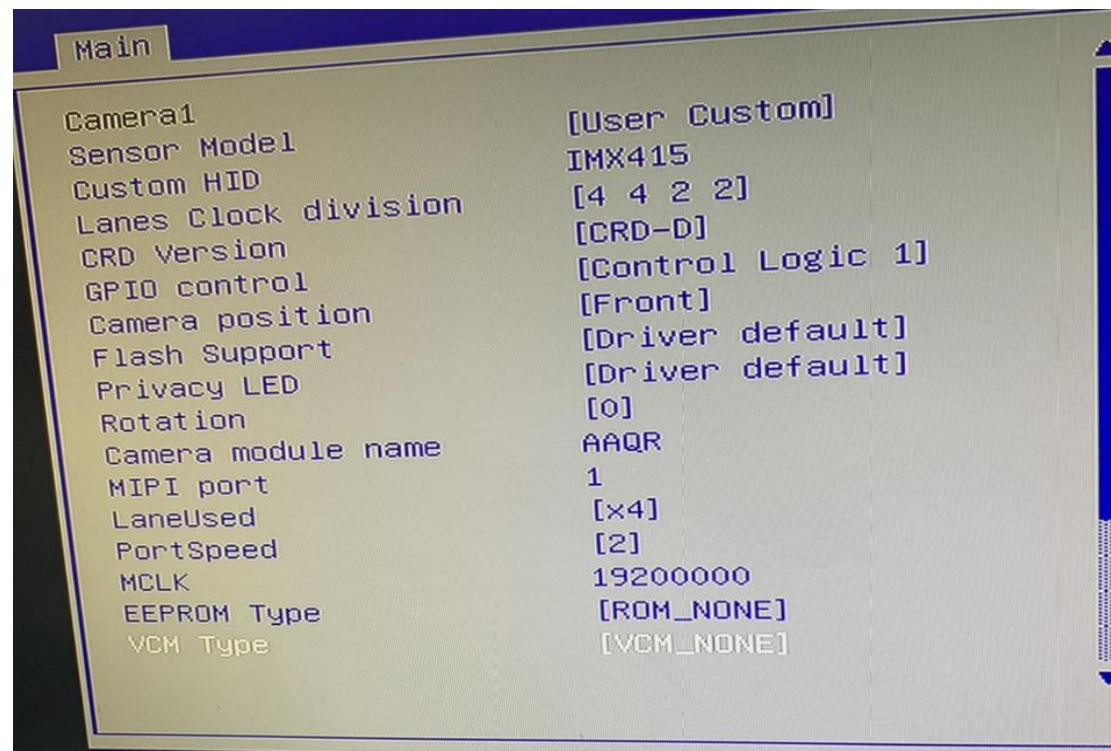
3-3 Back to main menu ➔ Camera 1: Enabled

3-4 Enter Link options



3-5 Link options

→ Follow the settings in the left picture below:

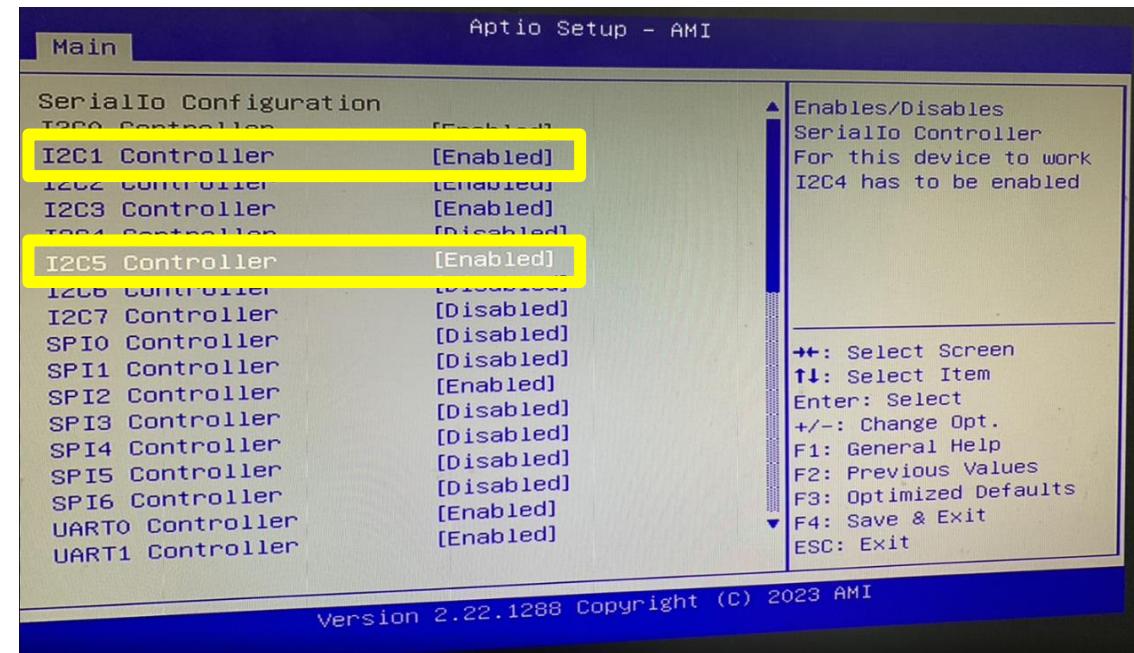
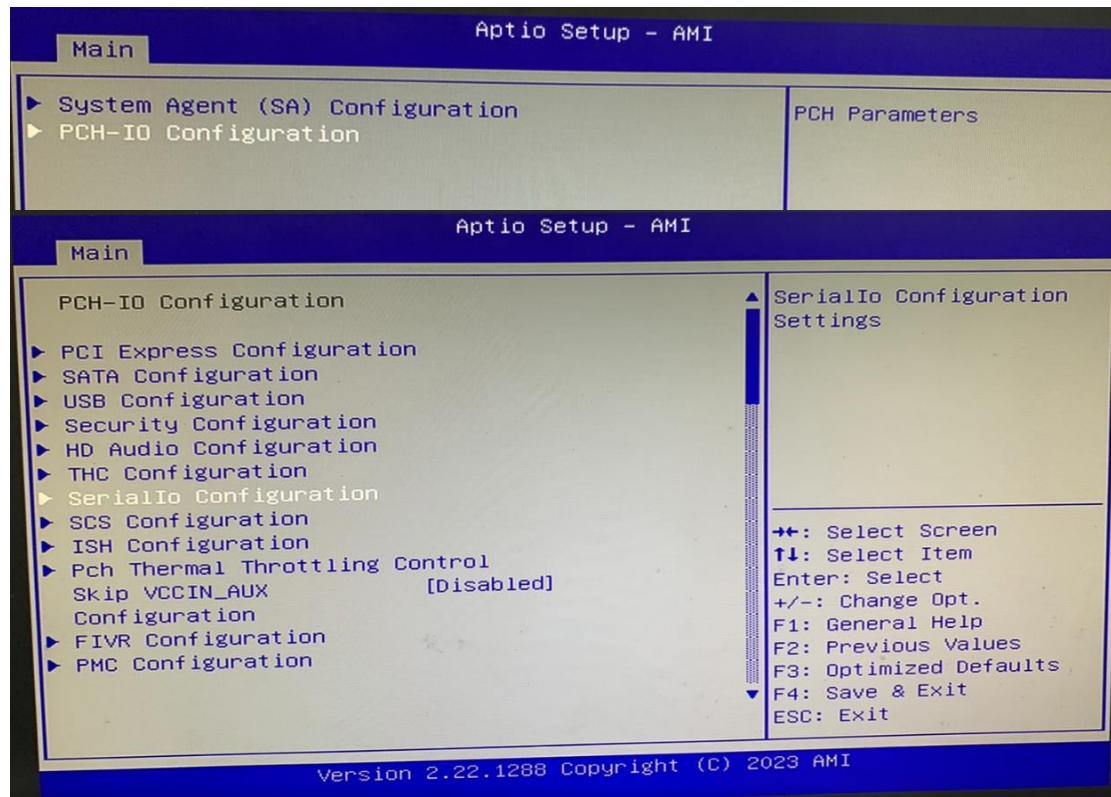


3-6 Back to main menu, enter PCH-IO

→ SerialIO Configuration

I2C1 Controller: Enabled

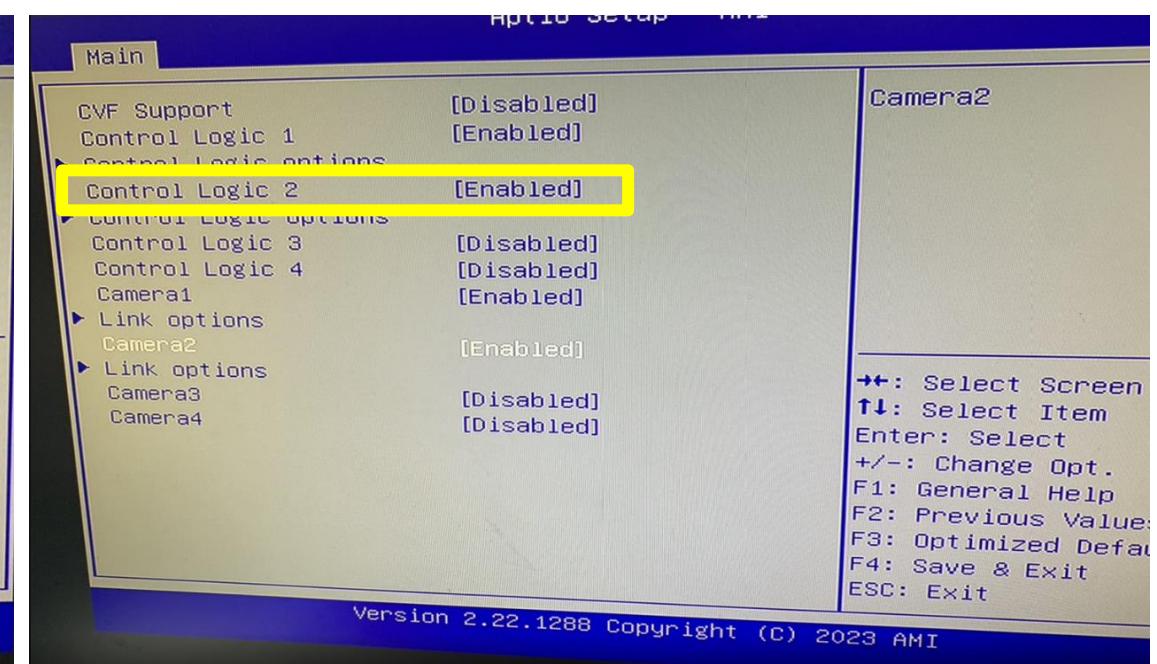
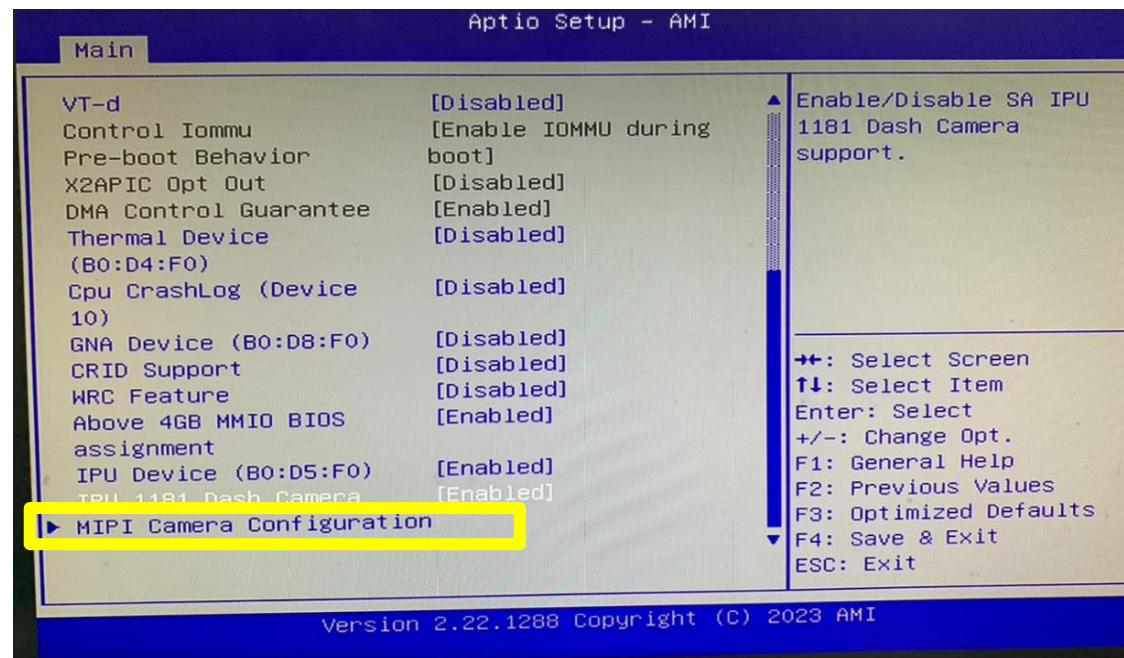
I2C5 Controller: Enabled



After all configuration is completed,
click F4 to save and reset.

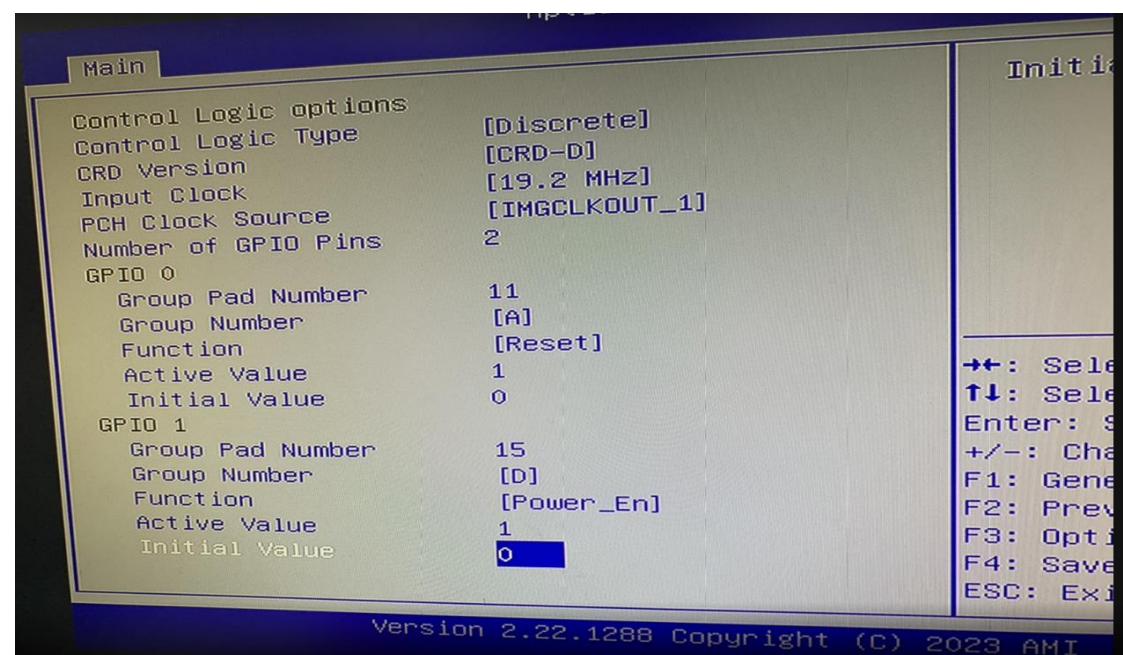
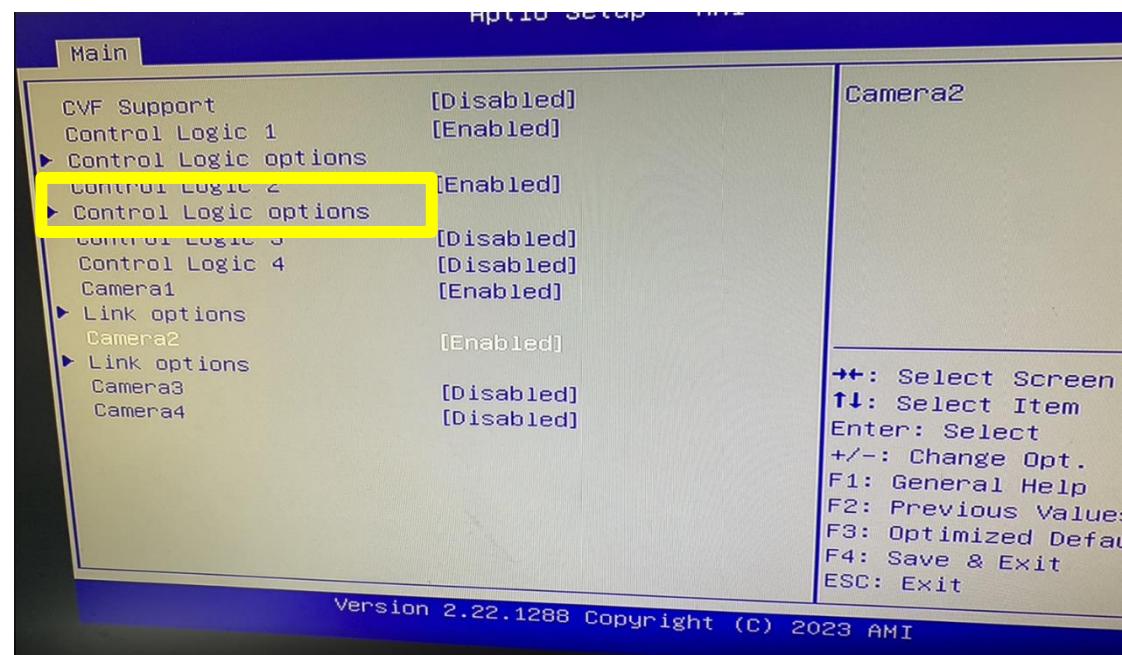
4. Enter MIPI Camera Configuration (for Camera 2)

4-1 Use Camera 2 ➔ Control Logic 2: Enabled



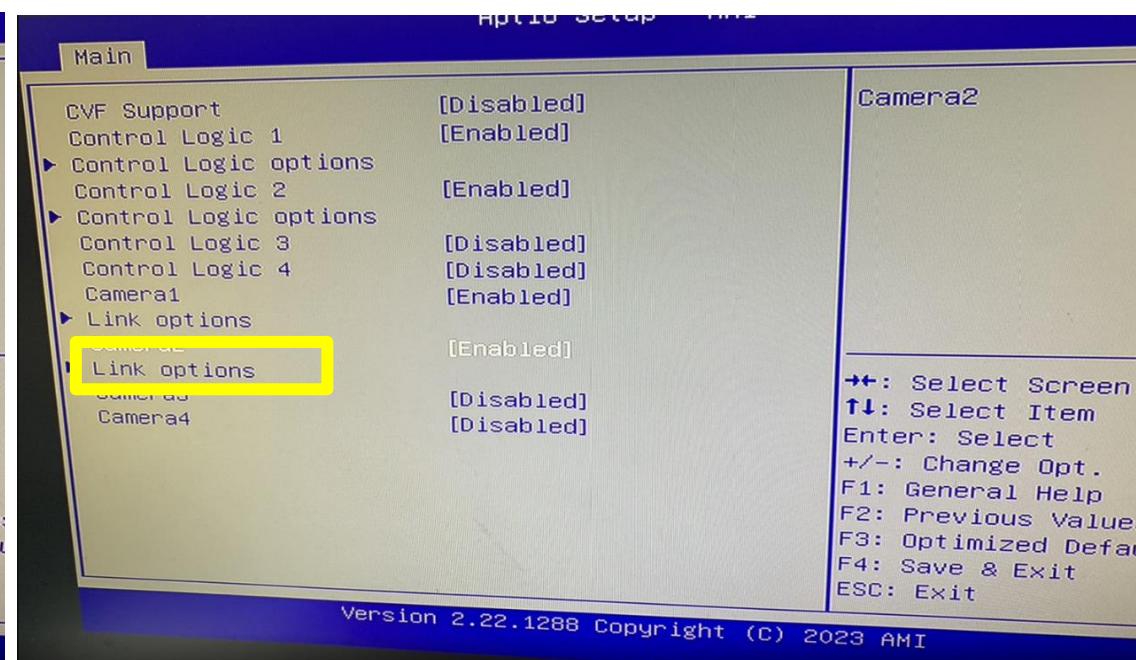
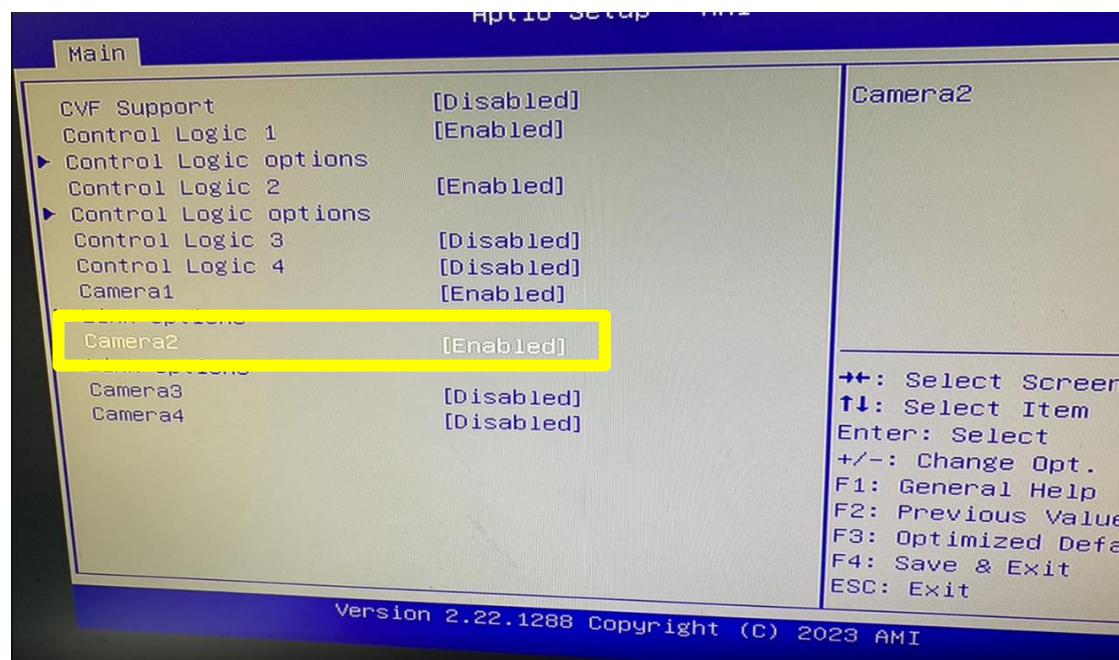
4-2 Control Logic Options

→ Follow the settings in the left picture below:



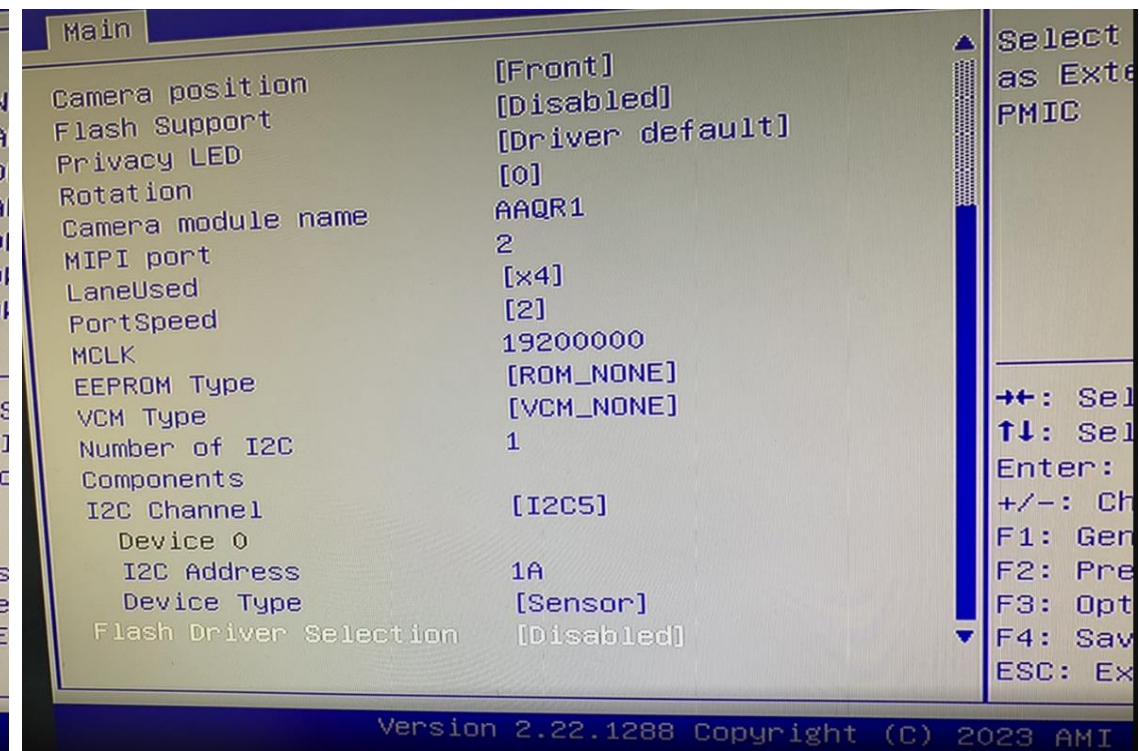
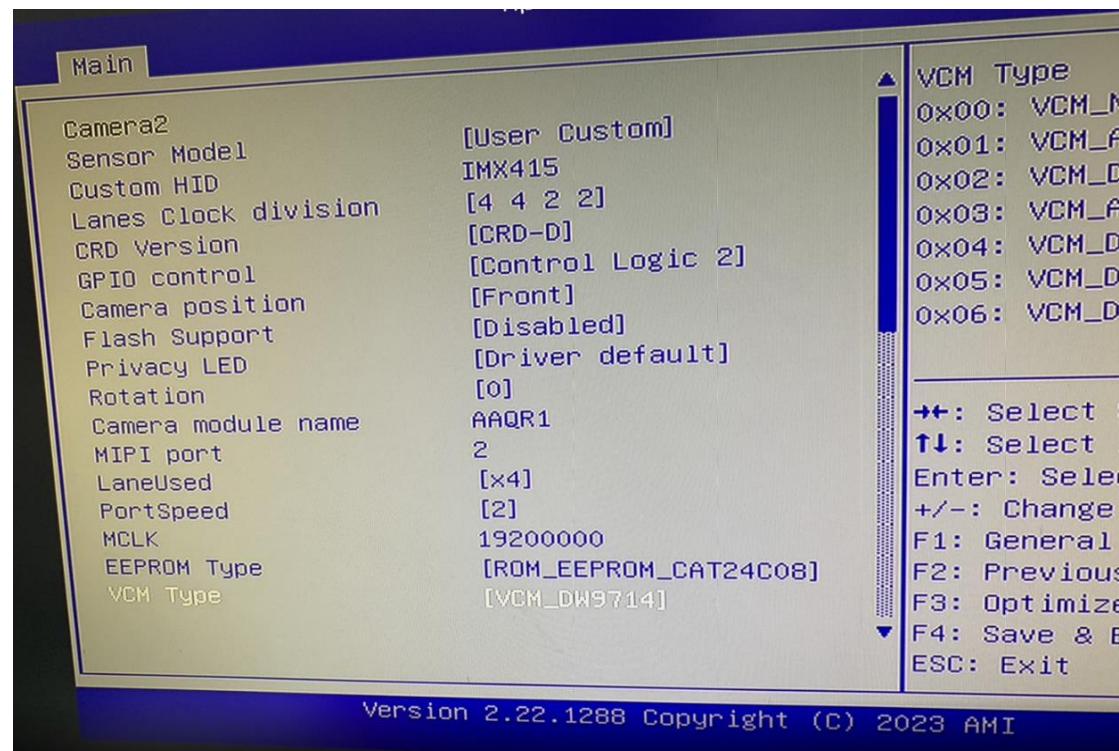
4-3 Back to main menu ➔ Camera 2: Enabled

4-4 Enter Link options



4-5 Link options

→ Follow the settings in the left picture below:

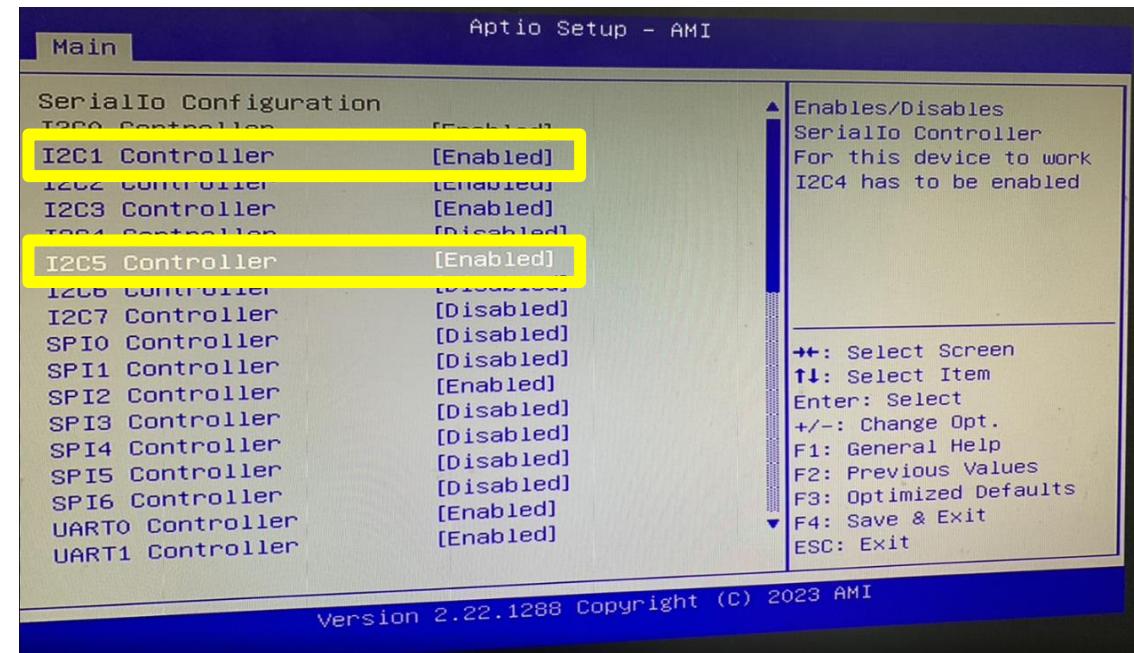
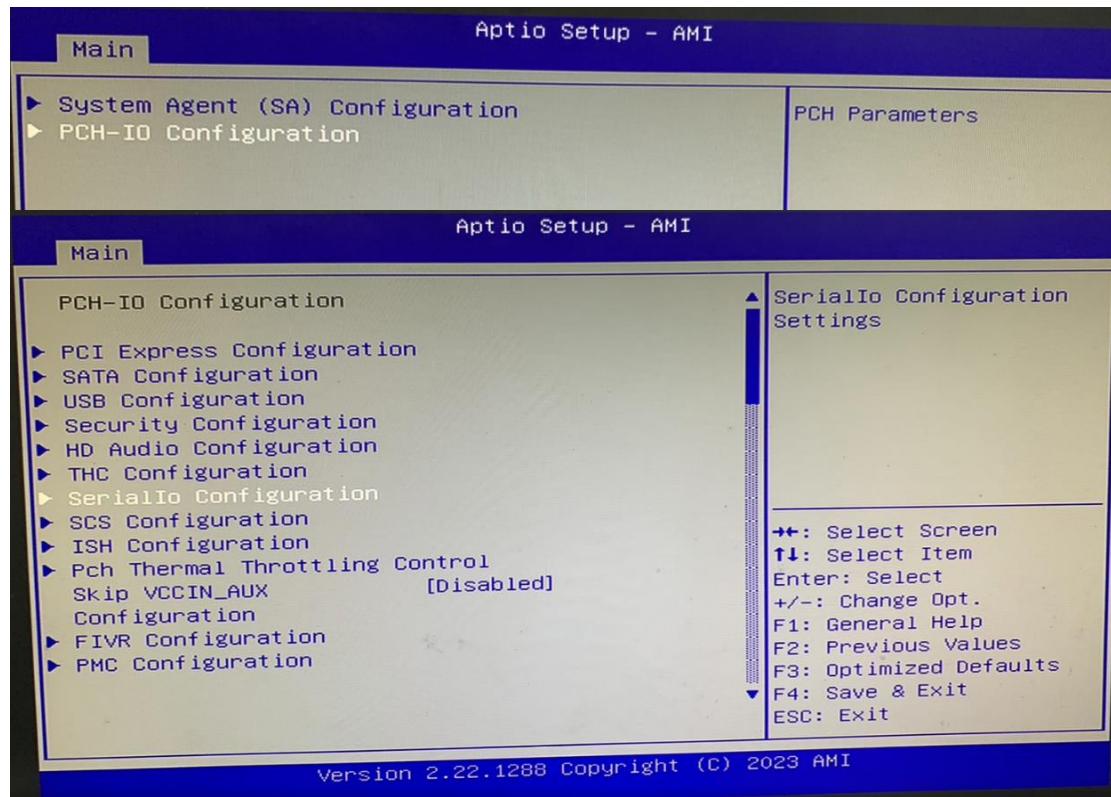


4-6 Back to main menu, enter PCH-IO

→ SerialIO Configuration

I2C1 Controller: Enabled

I2C5 Controller: Enabled



After all configuration is completed,
click F4 to save and reset.

The End