

M2-MYX80 Board Quick Installation Guide



REVISION HISTORY

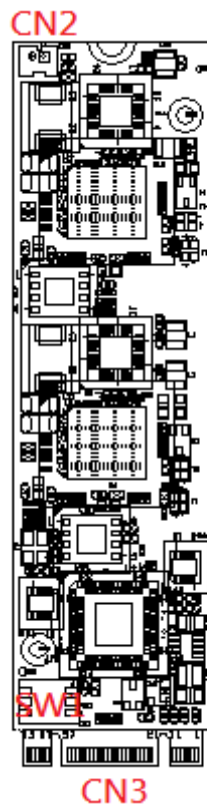
Revision	Date	Comments
A0.1	2018/11/12	Initial Version
A0.2	2019/02/15	Add Switch for Board ID

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1. M2-MYX80 V0.1 Board Layout

TOP



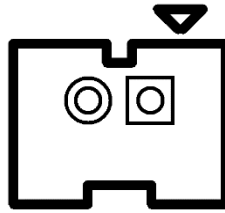
2. Connector References

Connector Index

Reference Designation	Functional Description	Connector Type
CN2	FAN power	(TF)WAFER BOX.2P.180D(M).DIP.1.25mm.PINREX.712-71-02TW01
CN3	M.2 (B+M key)	M.2 B+M key 75pin
SW1	Board ID	(TF)SWITCH.3S.6P.SOJ.24V.25mA.pitch=1.27mm.180D.SMD.BIWIN.SOJ03-R

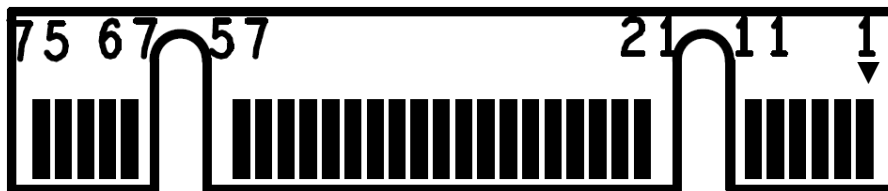
3. Connector Pin Assignments

CN2: FAN



Pin	Signal Description	Pin	Signal Description
1	5V	2	GND

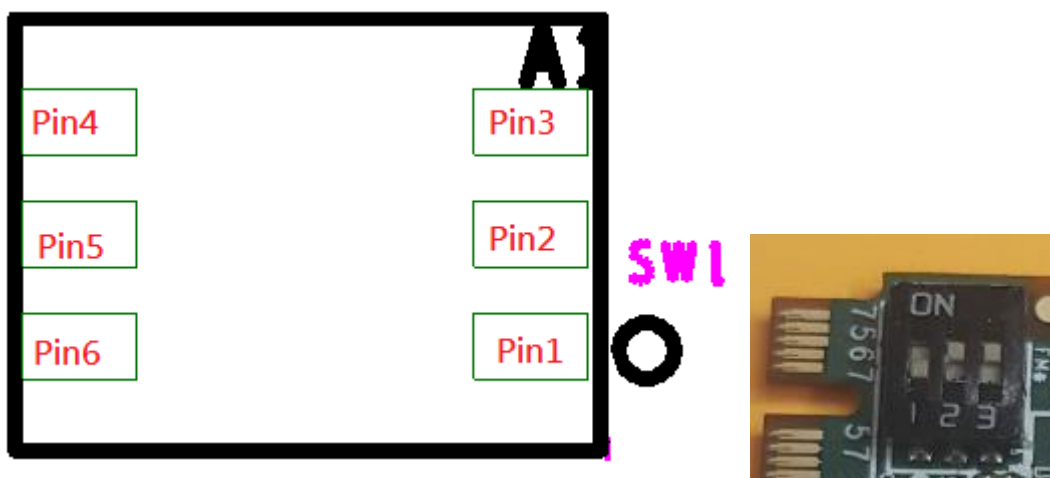
CN3: M.2 CONN B+M key



Pin	Signal Description	Pin	Signal Description
1	GND	39	GND
2	+3.3V	40	SOC_I2C1_SCL
3	GND	41	PCIE_TXN0
4	+3.3V	42	SOC_I2C1_SDA
5	NC	43	PCIE_TXP0
6	FULL_CARD_PWR_OFF#	44	NC
7	NC	45	GND
8	NC	46	NC
9	NC	47	PCIE_RXN0
10	NC	48	NC
11	NC	49	PCIE_RXP0
20	NC	50	PLT_RST#
21	GND	51	GND
22	NC	52	PCIE_CLKREQ#
23	NC	53	PCIE_CLK_N
24	NC	54	WAKE_M2
25	NC	55	PCIE_CLK_P
26	NC	56	NC
27	GND	57	GND
28	NC	58	NC

29	NC	67	NC
30	NC	68	NC
31	NC	69	NC
32	NC	70	+3.3V
33	GND	71	GND
34	NC	72	+3.3V
35	NC	73	GND
36	NC	74	+3.3V
37	NC	75	GND
38	VPU_EN		

SW1: Board ID



SW Value			Board ID
3	2	1	
Off	Off	Off	0
Off	Off	On	1
Off	On	Off	2
Off	On	On	3
On	Off	Off	4
On	Off	On	5
On	On	Off	6
On	On	On	7